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(Depositor's name)	Joyce Paoli	
(Signature)	Joyce Parli	
(Date)	2/14/06	

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/701,321	11/04/2003	Joung-Yeal Kim	5649-1169	5300

TITLE OF INVENTION: OUTPUT BUFFER CIRCUITS INCLUDING LOGIC GATES HAVING BALANCED OUTPUT NODES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE PUB		PUBLICATION FEE	TOTAL FEE(S) DUE		DATE DUE
nonprovisional	NO	\$1400)	\$300	\$	1700	03/13/2006
EXA	MINER	ART UN	IT	CLASS-SUBCLASS]		
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Samsung Electronics Co., Ltd.	Kyungki-do, Republic of Korea
Please check the appropriate assignee category or categories (will not be	printed on the patent):
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